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**[CITATION]** A monolithic 156 Mb/s clock and data recovery PLL **circuit** using the sample-and-hold technique

N Ishihara, Y Akazawa - IEEE Journal of Solid-State Circuits, 1994

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**[CITATION]** Low-jitter process-independent DLL and PLL based on self-biased techniques

JG Manea's - IEEE journal of solid-state circuits, 1996

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**RF power sensor having improved linearity over greater dynamic range**

SC Cripps, TR Allen, ... - US Patent 5,204,613, 1993 - Google Patents

... Greater input signal sensitivity is achieved since the voltages developed across the **bypass capacitors** 41, 42 ... 4, a schematic **circuit** diagram of an improved balanced detector is illustrated. ... This causes the input voltage standing wave ratio ("VSWR") of the sensor **circuit** to deterio ...

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**[PDF] Power supply noise reduction**

K Kundert - The Designer's Guide, 2004 - Citeseer

... problem can usually be minimized by tying the two supplies together with a **bypass capacitor**, as in ... A little effort taken during **circuit** board layout can save a lot of time and minimize ... Doubrava of Tektronix for sharing some of they deep experience of real world **design** issues with ...

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**[PDF] Three-stage large capacitive load amplifier with damping-factor-control frequency compensation**

KN Leung, PKT Mok, WH Ki, JKO ... - IEEE Journal of Solid- ..., 2000 - iarc.ee.nthu.edu.tw

... are short-circuited at high frequencies, and the high-frequency supply variations easily **bypass** through the ... the NMC amplifier driving a 1000-pF load requires unrealistic large compensation **capacitors** pF and ... [9] GC Temes and JW LaPatra, Introduction to **Circuit** Synthesis and ...

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**Microelectrode amplifier with improved method of input-capacitance neutralisation**

MV Thomas - Medical and Biological Engineering and Computing, 1977 - Springer

... The **bypass capacitors** C4 and C5 should have a very low impedance at high frequency; hence tantalum devices are recommended (normal electrolytic **capacitors** can have a significant series inductance). ... Table 1. Performance details of the **circuit** shown in Fig. ...

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DM Mitchell - IEEE Industry Applications Magazine, 1999

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**Radio-frequency impedance measurements using a tunnel-diode oscillator technique**

H Srikanth, J Wiggins, H Rees - Review of Scientific Instruments, 1999 - link.aip.org

... The resistors and make up a voltage divider and is a **bypass capacitor**. ... For C, surface mount high-Q rf chip **capacitors** (American Technical Ceramics) with values ranging from 470 to ... The LC **circuit** should be considered as an **circuit** because of the inherent internal **resistance** of ...

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**Pixel detectors with local intelligence: An IC designer point of view**

F Krummenacher - Nuclear Instruments and Methods in Physics Research ..., 1991 - Elsevier

... 3.2. Shaper An elementary shaper amplifier is shown in fig. 3a: the high-pass characteristic is implemented by means of **bypass capacitor** CHp and the low-pass transfer function can be realized with (parasitic) **capacitor** CLIP. ... II. **CIRCUIT DESIGN** 530 VSS Fig. ...

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**[CITATION]** A 90-dB SNR 2.5-MHz Output-Rate ADC Using Cascaded Multibit Delta-Sigma Modulation at 8 Oversampling Ratio

I Fujimori, L Longo, A Hairapetian, K Seiyama, S Kosic, ... - IEEE Journal of Solid-State ..., 2000

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